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Date:	November 22, 2004
To:	US Patent & Trademark Office 703-872-9306
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First Inventor:	Jian Zhou
Title:	Method for Automatically De-Skewing of Multiple Layer Wafer for Improved Pattern Recognition
Serial No.:	09/974,721
Examiner:	

Enclosed for filing in the above-identified application are the following:

## 1. STATUS INQUIRY (1 page)

Attorney Docket No: NAN050 US

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NOV 22 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Inventor: Jian Zhou  
Assignee: Nanometrics Incorporated  
Title: Method for Automatically De-Skewing of Multiple Layer Wafer for Improved Pattern Recognition  
Serial No.: 09/974,721 Filing Date: October 9, 2001  
Examiner: Group Art Unit: 1765  
Docket No.: NAN050 US Confirmation No.: 7841

Santa Clara, California  
November 22, 2004

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST FOR STATUS OF APPLICATION**

Sir:

Applicant(s) hereby request(s) a report on the status of the above-identified patent application. If a telephone call would expedite this request, please telephone the undersigned attorney at (408) 982-8202.

I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office to the fax number 703-872-9306 on November 22, 2004.

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Attorney for Applicant(s)

Respectfully submitted,

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